



# CAD inputs

This article lists CAD systems supported by the application



## Four types of interfaces are available

Layout description (RPDATA, FATF, ...)

There are two possible approaches:

The application provides direct importers using CAD files in native format. Dedicated scripts installed on the ECAD system export CAD data in order to be used directly by the application. This solution is used when CAD files format is not stable enough.

Layout	Company	Interface Name	Files Type	CAD importer	CAD preproc.
ACCEL PCB	Altium	P-CAD	*.pcb, *.asc	<input checked="" type="checkbox"/>	
Allegro	Cadence	CADENCE	pcb, .cad, .fab	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
Altium Designer	Altium	ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
		P-CAD	*.pcb, *.asc	<input checked="" type="checkbox"/>	
		PROTEL	*.pcb	<input checked="" type="checkbox"/>	
ARES Proteus	Labcenter Electronics	ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
ASTER Quick Start	ASTER Technologies	AQS	.aqs	<input checked="" type="checkbox"/>	
Bartels Auto Engineer (BAE)	Bartels System GmbH	GENCAD	.cad	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
BoardStation BoardStation XE FabLink XE	Mentor Graphics	NEUTRAL	.nf, .neu, .vss, *.fm, neutral_file*	<input checked="" type="checkbox"/>	
		GENCAD	.cad	<input checked="" type="checkbox"/>	
		ODB++	tgz, .tar, .gz		



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Cadlink II	ASTER Technologies	RPDATA	rpdata.asc	<input checked="" type="checkbox"/>	
Cadstar PCB Design	Zuken	GENCAD	.cad	<input checked="" type="checkbox"/>	
		CADIF	.paf	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz,.zip	<input checked="" type="checkbox"/>	
CAMCAD	Router Solutions Inc	CAMCAD	.cc, .ccz	<input checked="" type="checkbox"/>	
CR3000 PWS	Zuken	CADIF	.paf	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz,.zip	<input checked="" type="checkbox"/>	
CR5000 Board Designer	Zuken	CR5000-BD	.pcf, ftf	<input checked="" type="checkbox"/>	
		CADIF	.paf	<input checked="" type="checkbox"/>	
		GENCAD	.cad	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz,.zip	<input checked="" type="checkbox"/>	
Docica 3.2&4.1	Alcatel	DOCICA	.doc	<input checked="" type="checkbox"/>	
Eagle CAD	CadSoft	FATF	.fat, fatf.asc, *.far, *.faz	<input checked="" type="checkbox"/>	
		GENCAD	cad	<input checked="" type="checkbox"/>	
EDWinXP	Visionics	GenCAM Standard	.gcm	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz,.zip	<input checked="" type="checkbox"/>	
Enterprise 3000	Valor	ODB++	.tgz, .tar, .gz,.zip	<input checked="" type="checkbox"/>	
Expedition PCB	Mentor Graphics	GENCAD	.cad	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz,.zip	<input checked="" type="checkbox"/>	



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Gencad Data	Teradyne (ex Genrad)	GENCAD	.cad, .gcd	<input checked="" type="checkbox"/>	
GenCAM Standard	IPC	GenCAM Standard	.gcm	<input checked="" type="checkbox"/>	
IPC-2581	IPC	IPC2581	*.cvg, *.xml	<input checked="" type="checkbox"/>	
IPC-356	IPC	IPC356	*.ipc, *.356	<input checked="" type="checkbox"/>	
i3070	Agilent	i3070	board	<input checked="" type="checkbox"/>	
Migger	Larisys	FATF	.fat, fatf.asc	<input checked="" type="checkbox"/>	
ODB++	Valor	ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
ODB++XML	Valor	ODB++	.xml.gz	<input checked="" type="checkbox"/>	
OrCAD Layout	Cadence	OrCAD	.min	<input checked="" type="checkbox"/>	
		GENCAD	.cad	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
PADS Power PCB	Mentor Graphics	PADS	.asc	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
Pantheon	Intercept Technology Inc	GENCAD	.cad	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
P-CAD	Altium	P-CAD	*.pcb, *.asc	<input checked="" type="checkbox"/>	
		PDIF	.pdf	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
Protel	Altium	PROTEL	*.pcb	<input checked="" type="checkbox"/>	



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		ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
Pulsonix	Westdev Ltd	GENCAD	.cad	<input checked="" type="checkbox"/>	
		ODB++	tgz, .tar, .gz	<input checked="" type="checkbox"/>	
Seetrax Ranger	Seetrax CAE Ltd.	GENCAD	.cad	<input checked="" type="checkbox"/>	
Supermax ECAD	Dansk Data Elektronik (DDE) Mentor Graphics	ODB++	tgz, .tar, .gz	<input checked="" type="checkbox"/>	
TestExpert (Fabmaster)	UGS Tecnomatix	FATF	.fat, fatf.asc	<input checked="" type="checkbox"/>	
TestSight	DeMille Research	GENCAD	.cad	<input checked="" type="checkbox"/>	
Trilogy 5000	Valor	ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	
UNICAD	Cadence	UNICAD	.arc, .txt	<input checked="" type="checkbox"/>	
Unicam 6.2	UGS Tecnomatix	RPDATA.DLL			<input checked="" type="checkbox"/>
Veribest PCB	Intergraph	GENCAD	.cad	<input checked="" type="checkbox"/>	
Visula	Zuken	CADIF	.paf	<input checked="" type="checkbox"/>	
		ODB++	.tgz, .tar, .gz, .zip	<input checked="" type="checkbox"/>	



ISO 9001 zertifiziert

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