PicoTAP Designer Studio
Your fast start to the benefits of testing with JTAG/Boundary Scan

- Complete hardware and software package for IEEE 1149.x
- Graphical project development supported by Mission Assist™
- Automated test generation and pin-failure diagnosis
- Debug of BGA pins and embedded nets
- On board device programming
- Ideal for design verification and production testing

Boundary Scan (IEEE Std. 1149.x) is the established technology of substituting physical access via nails and probes through a special on-chip electronics (electronic nails) in conjunction with a dedicated four-wire test bus. The method is well-established, with most complex devices now having the inbuilt support enabling the method to be used as an integral part of modern test strategy — as stand-alone solution or in addition to existing electric and optical test systems. PicoTAP Designer Studio provides a cost-efficient entry level solution which still contains all necessary features for powerful and convenient test program generation, execution and failure diagnosis incl. interactive debugger. System upgrades are possible to take advantage of more powerful/faster GOEPEL electronic systems and extensive additional software for test, programming and debugging automation.
## Hardware and Software Components

PicoTAP Designer Studio provides an excellent price-performance ratio by utilizing a USB controller in combination with a mixed-signal I/O module with special editions (Base/SX or Standard/SX) of the world’s leading Boundary Scan software platform SYSTEM CASCON™. 

Features include:
- PicoTAP controller (USB 2.0) with plug-in connector
- CION Module/FXT-96A with analogue and digital I/Os
- Add-on relays and opto-electronic I/Os
- SYSTEM CASCON platform license incl. run-time
- All CAD Readers and Device Libraries
- Design and Testability Explorer
- Multi Board Merger
- Mission Assist for graphical project development
- Automated test generation for interconnections
- Automated pin-failure diagnosis for interconnections
- Unlimited manual test generation incl. diagnosis
- ScanAssist™: Interactive Pin Toggler
- ScanAssist™: Multi Mode Debugger
- Programming of PLD and moderate Flash*
- Graphical Test Coverage Analyzer (TCA)*
- ATPG/pin-fault diagnosis for memories and clusters*
- One-year maintenance contract

*available only in "Standard Version"

All packages are prepared for a multitude of Boundary Scan test applications but can be extended/upgraded depending on requirements.

## Extension in various Directions

Both hardware and software can be upgraded at any time. Significant options worth consideration:
- Add-on CION I/O modules (TAP cascading)
- Additional Automated Test Generators (ATPG) and Automatic pin-fault diagnosis (PFD)
- Visualizer for layout and schematic (ScanVision™)

Extension options are simply activated by new license files.

## Product Support and Order Information

PicoTAP Designer Studio includes a one-year maintenance contract. That provides access to the worldwide support and service network and free software updates.

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<th>Product</th>
<th>CASCON Package</th>
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<td>PicoTAP Designer Studio Basic Version</td>
<td>DS Base/SX</td>
<td>SHB-030</td>
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<tr>
<td>PicoTAP Designer Studio Standard Version</td>
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Additional information can be found at [goepel.com](http://goepel.com).

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**Hardware at a glance**

PicoTAP controller for USB 2.0

- 96 digital I/Os
- 12 analogue I/Os

CION Module FXT-96/A

**Graphical development tools in SYSTEM CASCON™**

PicoTAP Designer Studio is a complete Boundary Scan tester.