



# PicoTAP ATE

## Ultra-compact JTAG/Boundary Scan Controller



- Controller for embedded testing and programming
- Low-cost module with high performance and multifunctional architecture
- available in a housing or as a PCBA for space-saving installation in the test system
- integrated compact unit controllable via USB 2.0



Parameter	
number of TAPs	1 (integrated TIC 020)
maximum TCK frequency	15 MHz (adjustable via software)
I/O channels	4 AUX channels
maximum number of I/O modules	-
dimensions	60 mm x 45 mm x 12 mm

## Embedded Test

- support of the latest technologies such as Processor Emulation Test, FPGA Assisted Test and Embedded Diagnostics Test
- synchronisation with the ChipVORX FPGA instruments

## Software

- integration in Embedded JTAG Solutions SYSTEM CASCON™ platform
- plug-and-play integration with third-party systems
- open mix of test and programming procedures in one environment

## Performance

- up to 15 MHz TCK frequency
- 4 additional AUX signals
- integrated TIC020 module

## Embedded Programming

- programming of flash components such as NAND, NOR, SPI, I<sup>2</sup>C, eMMC, etc.
- universal programming of µControllers
- programming of FPGA/PLD

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Boundary Scan Controller



## Adaptability

- quick and easy integration into test systems
- no additional power supply necessary
- software-selectable protocols (JTAG, DAP, SWD, BDM, ...)

## Expandability

- parallel capability through asynchronous testing with multiple controllers (separate CASCON licences required)

Made in Germany