ChipVORX Synthetic Instruments
FPGA test designs configuring instead of programming

- technology for universal chip-embedded FPGA control
- real-time interconnection test, stress test and margin test for DDR 2/3/4
- High-speed Flash programming
- Ethernet stress test and more instruments
- individual synthetic instrumentation, optimized for your design
- simple configuration & synthetization without the need for expert knowledge
- completely integrated in SYSTEM CASCON tool suite
**Available functions**
- universal frequency measurement/event counter
- Flash programming
- real-time RAM interconnection, stress and margin test for DDR 2/3/4
- Ethernet stress test (Frame Error Rate Test)
- Bit Error Rate Test
- Eye/Shmoo diagrams
- IO conditioning, IEEE 1149.8.1, CAN/LIN + stress test, UART, HDMI, DisplayPort and more instruments

**Control of**
- IEEE 1687 instruments
- proprietary Instruments
- configurable Instruments (FPGA)
- interactive tests with Boundary Scan

**Reconfigurable IPs**
- adaption on the target function

**High performance**
- up to factor 100 faster than Boundary Scan

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**Synthetic Instrumentation**
- optimized instrument performance (DDR3/4 at speed tests)
- more instrument types (DDR margin tests)
- combined instruments into single IP

**FPGA Support for**
- Intel/Altera
- Lattice
- Xilinx
- etc

**Made in Germany**