

GOEPEL electronic's Boundary Scan Integration into Concord Technology In-Circuit Tester



Combine the advantages of In-Circuit Test and JTAG/Boundary Scan to get the best possible test coverage!

Advantages of In-Circuit Test

- Short test time
- Testing for short/opens
- Test of passive components
- Test of discrete logic
- Test of analogue components

Concord Technology Supertest T800FV

- High-speed accurate test increases throughput and improves PCBA quality and reliability
- Low-cost system user-friendly for unskilled operators helps reducing costs
- LAN support for statistic process control provides total solution

Advantages of JTAG/Boundary Scan

- Standardised test method since 1990
- Test of highly complex digital devices like microprocessors, FPGAs and PLDs
- Test of memory and flash
- PLDs, EEPROMs and flash

SCANBOOSTER™/USB-FXT Designer Studio and Run Time Studio

- Excellent price-performance ratio
- Multiple application levels (IC, ISP, system)
- Flexible and expandable architecture

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