G-FIP Flash In-System Programming (ISP) via Automotive Busses



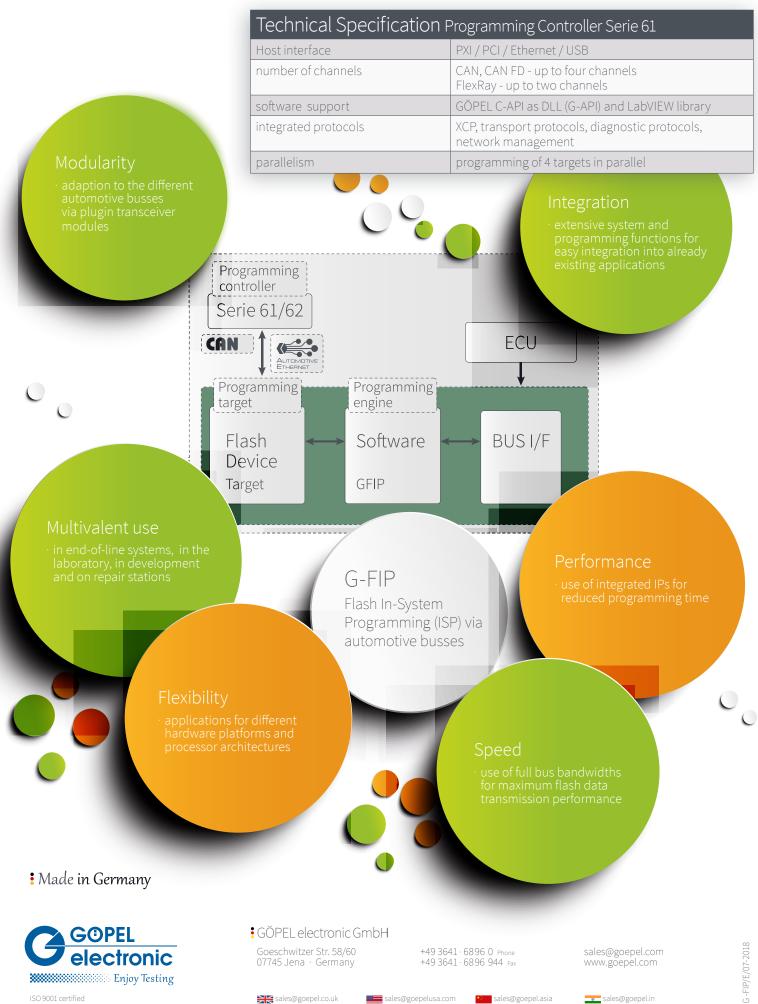
- high-performance Flash programming of control units in end-of-line systems, in the laboratory, in development and on repair stations
- · maximum use of available automotive bus bandwidth
- · programming and verification of Flash data via specific Flash IPs
- \cdot Flash algorithm not required to remain permanently in the control unit
- · wide range of hardware interfaces available
- · easy integration into existing software environments
- \cdot support of various automotive busses and processor platforms





Technical Specification

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