



Embedded JTAG Solutions

Embedded Programming,
Embedded Board Test und Embedded Functional Test



- No space for test points?
- Do you want to put your prototypes into operation quickly – and even without firmware?
- Is the size of your data causing long programming times?
- Are you facing decreasing test coverage with your existing testing capabilities?
- Do you need more detailed fault diagnostics?
- Do you prefer standardized test and programming for both development and production?

Embedded JTAG Solutions

Since the introduction of the 1149.1 standard in 1990, GÖPEL electronic has been working on hardware and software solutions that utilize the JTAG interface for testing board connections and functionality. Over the years, additional extensions to the standard and test technologies have been added, which are now grouped together under the term Embedded JTAG Solutions.

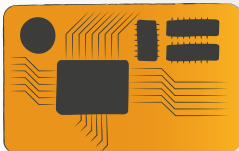
But what are these Embedded JTAG Solutions? Embedded JTAG Solutions consist of a total of three application areas.



Embedded Functional Test

Embedded Functional Test

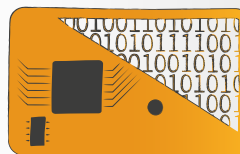
Today's test strategies now require more than just testing board connections. In addition to the traditional task of assuring perfect connectivity, Embedded JTAG Solutions provides Embedded Functional Test to also assure correct board and component functionality.



Embedded Board Test

Embedded Board Test

Embedded JTAG Solutions also provides Embedded Board Test for verification of functional board connections. With this capability, Boundary Scan, microcontroller and FPGA resources are used to find shorts, non-soldered pins and pull resistors.



Embedded Programming

Embedded Programming

The growing demand for and challenges involved with in-system programming of a variety of data often represents a major hurdle today, especially with increasing file sizes and growing demands on programming speed. Embedded JTAG Solutions addresses such challenges by enabling the use of on-board resources to program at high speed.

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