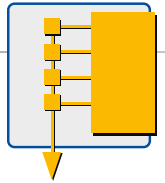


SCANBOOSTER™ Designer Studio

Your Introduction to JTAG/Boundary Scan



- ✓ Complete hardware and software package for IEEE 1149.x
- ✓ Graphical project development supported by Mission Assist™
- ✓ Automated test generation and pin-failure diagnosis
- ✓ Debug of BGA pins and embedded nets
- ✓ Suites for design verification and production test

Boundary Scan (IEEE Std. 1149.x) is a revolutionary technology substituting physical access via nails and probes by means of special on-chip electronics (electronic nails) in conjunction with a dedicated four-wire bus. The method has been well established as an integral part in modern test strategies – as both a stand-alone solution and a complement to existing electric and optical test systems.

SCANBOOSTER™ Designer Studio provides a cost-efficient entry level solution which still contains all necessary features for powerful and convenient test program generation, execution and failure diagnosis incl. interactive debugger. The system can be easily upgraded at any time.

SCANBOOSTER™ Designer Studio

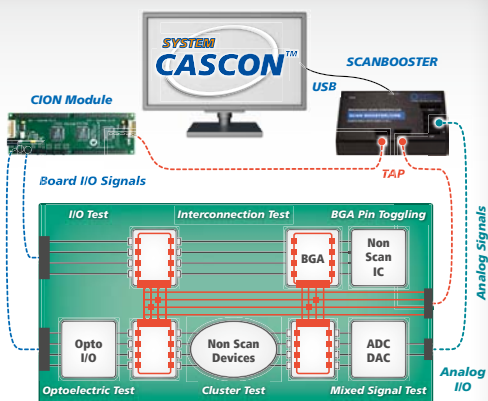
SCANBOOSTER/USB at a glance



SCANBOOSTER controller for USB 2.0



Graphical development tools in SYSTEM CASCON™



Complete Boundary Scan tester with option for CION Modules

Hardware and Software Components

SCANBOOSTER Designer Studio provides an excellent price-performance ratio by utilizing a convenient controller in combination with a special edition (Base/SX or Standard/SX) of the worldwide leading Boundary Scan software platform SYSTEM CASCON™. Features include:

- SCANBOOSTER controller (USB) with TIC01
- Programmable VIO (1.8 V – 5 V) and impedance
- Add-on analogue and digital I/O
- SYSTEM CASCON platform license incl. run-time
- All CAD Readers and Device Libraries
- Design and Testability Explorer
- Multi Board Merger
- Mission Assist for graphical project development
- Automated test generation for interconnections
- Automated pin-failure diagnosis for interconnections
- Unlimited manual test generation incl. diagnosis
- ScanAssist™: Interactive Pin Toggler
- ScanAssist™: Multi Mode Debugger
- Programming of PLDs and moderate Flash*
- Graphical Test Coverage Analyzer (TCA)*
- ATPG/pin-failure-diagnosis for memories and clusters*
- One-year maintenance contract

* available in "Standard Version" only

The package is well prepared for a multitude of Boundary Scan test applications. Depending on the demands, it can be extended even to high-end applications.

Extension in various Directions

Both hardware and software can be upgraded at any time.

Particular options to be named are:

- Different TIC modules (e.g. for greater UUT distances)
- CION I/O modules (e.g. for connector tests)
- Additional Automated Test Generators (ATPG)
- Visualizer for layout and schematic (ScanVision™)

The enhancement options are activated easily by a new license file.

Product Support and Oder Information

SCANBOOSTER Designer Studio includes a one-year maintenance contract. This ensures access to the worldwide support and service network. Software updates are available free of charge.

Product	CASCON Package	PN
SCANBOOSTER Designer Studio Basic Version	DS Base/SX	SHB-020
SCANBOOSTER Designer Studio Standard Version	DS Standard/SX	SHB-021

Additional information can be found at goepel.com.



ISO 9001 certified

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Authorised Distributor:

GOEPEL electronic GmbH
Goeschwtzer Str. 58/60

• 07745 Jena / Germany

Phone: +49(0)-3641-6896-0

Fax: +49(0)-3641-6896-944

Email: sales@goepel.com

Web: www.goepel.com

sales@goepel.co.uk

sales@goepelusa.com

sales@goepel.asia

sales@goepel.fr

sales@goepel.in